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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named

Inventor : Ian M. Flanagan et al.

Appln. No.: 09/879,416 /

Filed : June 12, 2001

For : DELAY-LOCKED LOOP WITH BUILT-
IN SELF-TEST OF PHASE MARGIN

Docket No.: 01-191/L13.12-0157

Group Art Unit: 2637

Examiner:

Aslan Ettihadieh

AMENDMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

I HEREBY CERTIFY THAT THIS PAPER IS
BEING SENT BY U.S. MAIL, FIRST CLASS, TO
THE COMMISSIONER FOR PATENTS, P.O. BOX
1450, ALEXANDRIA, VA 22313-1450, THIS

19 DAY OF December,
2005

David H. Dr.
PATENT ATTORNEY

Sir:

This is in response to the Office Action dated
September 19, 2005. Please amend the above-identified application
as follows.